

Available Research Practice Projects
(ME Microelectronics/ ME Embedded Systems/ ME Communication Engineering)

Name of Faculty	Topics	Area: Comm/micro/embedded	Expected outcome
Prof. Navneet Gupta	Design and Analysis of Metamaterial based Antennas for RF devices	ME Communication	Publication in a standard IEEE conference/SCOPUS indexed journal
	Modeling and Simulation of carbon nanotube field-effect transistors (CNT-FET)	ME Microelectronics	
Prof. Chandra Shekhar	VLSI Architectures and VLSI testing	ME Microelectronics/ ME Embedded System	Strong understanding of architectures
Prof. V. K. Chaubey	Communication link / Network modeling & Design	ME Communication	To identify a research problem
Prof. Anu Gupta	Implementation Of ANN	ME Microelectronics/ Embedded System/ Communication	
	Radiation Hardened Circuits		
	Dynamic Voltage Frequency Scaling		
	Clock Generation And Recovery		
Prof. Hari Om Bansal	FPGA-Based Control for Electric Vehicle and Hybrid Electric Vehicle	ME Embedded	Validation of control algorithm for HEVs to improve fuel efficiency. Publication in a reputed journal
	Development of Control Technique for Shunt Active Power Filter using Xilinx toolbox		Validation of control algorithm for SAPF to Reduce THD. Publication in a reputed journal
Prof. H.D.Mathur	IoT applications in Smart Grid Scenario	ME Embedded System	Development of a prototype for some application
Prof. Dheerendra Singh	FPGA implementation of Various speed Control Tech.	ME Microelectronics/ Embedded/ Communication	Real time learning + Publication in good Journal
	Development of Deep Learning-Based crowd estimation,/ feature extraction.		
	FPGA implementation of ANN-based Active filtering		
Dr. Abhijit Asati	Study of clock domain crossing in VLSI circuits	Microelectronics	(i) Understating CDC issues (ii) Simulating CDC (iii) Writing and communicating a Research paper
Dr. K K Gupta	Structural Health Monitoring based on Cyber-Physical System	ME Embedded / Communication / Micro	Publication in a standard IEEE conference/SCOPUS indexed journal
	Smart Water Grid		

	<p>Integrated Multi-Sensor Array for Water Quality Assessment</p> <p>Compressed Domain Video Analysis</p> <p>Multimodal Biometric Techniques using thermal and visible Facial Images</p> <p>Bearing Health Monitoring based on vibration and</p>		(based on the quality of research and knowledge advancement)
	acoustics		
Dr. Rahul Singhal	<p>Design of Optical Communication Networks</p> <p>Planar Antenna Design & Development</p> <p>Design Optical Waveguide Based Devices</p>	ME Communication	National/ International conference
Dr. Praveen Kumar A.V.	<p>Dual-feed RF coupler designs</p> <p>Characterization of edible oil using microwaves</p> <p>FDTD analysis of slot antenna</p>		Minimum Outcome to submit a conference paper
Dr. Anantha Krishna Chintanpalli	<p>Concurrent vowel identification using neural networks.</p> <p>EEG signal analysis using a time-frequency representation.</p>	ME Communication	Journal (Scopus/SCI)/IEEE conference
Dr. Sainath Bitragunta	<p>Energy efficient and delay constrained cognitive radio network: design, analysis, and, simulation</p> <p>Cooperative and cognitive satellite systems: efficient protocol design and analysis</p> <p>Energy harvesting Millimeterwave communication system design and performance analysis</p> <p>Application of stochastic geometry for efficient wireless network modeling and design</p>	ME Communication	Journal (Scopus/SCI)/IEEE conference
Dr. Nitin Chaturvedi	<p>Design of Nonvolatile SRAM cell for storing multiple bits for runtime context switching for IoT</p> <p>Design of Self-Resetting Latches for Asynchronous Micro-Pipelines</p>	ME Microelectronics	Good quality conference/journal paper (SCOPUS indexed)

	Study/Design and analysis of high-speed asynchronous write circuit for non-volatile memory and logic		
	Study and analysis on the potentials of FinFETs for Asynchronous Circuit Design		
	Design and analysis of reconfigurable cache architecture and cache coherence protocols		
	Study/Design of GaN HEMT Device for biosensing applications		
	Study/Design of GaN HEMT Device for high voltage applications		
Dr. Arnab Hazra	Self-doped TiO ₂ Nanotube	ME Microelectronics	Good Publication
	Sensor for Low-Temperature Vapour Detection		
	Fabrication and Characterizations of Cu ₂ O/TiO ₂ Heterojunction for Vapor Sensing Application		
Dr. Pankaj Arora	Plasmonic devices for optical sensing	ME Microelectronics ME Communication	Good quality journal paper /International Conference
	Plasmonic solar cells		
Dr. Vinay Chamola	Developing real-time applications for the Internet of Things.	Embedded	Working models implementing IoT
	Fog Computing for the Internet of Things.	Embedded	Good quality Journal/Conference paper (+ hardware implementation)
	Research Frontiers in the Internet of Things.	Embedded/Communications	Good quality survey paper/ Literature review
	Applications connecting smartphones to the IoT and Cloud.	Embedded	Working models implementing IoT
	Energy and Delay aware resource management for solar-powered cellular networks	Communications	Good quality Journal/Conference paper
Dr. G S Sessa Chalapathi	Integrated Time Synchronization and ranging algorithm implementation	ME Embedded/ Communication	Scopus Index Journal paper
	Survey of existing Time Synchronization protocols in Wireless Sensor Networks		

	Implementation of time synchronization algorithm of Wireless Sensor Nodes on the hardware platform		
Mr. Devesh Samaiya	Development of Low Power, Small size, Embedded Anti-Theft Device	ME Embedded System	Research paper
	Study of background		
	subtraction methods in videos with highly dynamic background content		
	Study of camera motion estimation and compensation techniques in video sequences.		
Mr. K. Babu Ravi Teja	1.ASIC Implementation of Video compression techniques (optimization at submodule level)	Knowledge of HDL, image processing, Xilinx Tools	Publication in a good conference
	2.Investigation of Design strategies for multi-gate transistors	Good understanding of Digital VLSI Design, SPICE	Publication in a good conference
	3.FPGA based implementation of RNS based DSP systems	Knowledge of HDL, Digital Design	Publication in a good conference
	4.ASIC Implementation of Turbo Decoders	knowledge of HDL, digital design, and communication systems	Publication in a good conference
Dr. Ashish Patel	1.FPGA based control of power converters and its validation using hardware in the loop simulation	Embedded	Scopus/SCI indexed publication