

BITS Pilani
Hyderabad Campus
Department of EEE

21st NATIONAL CONFERENCE ON EMERGING TRENDS IN VACUUM ELECTRONIC DEVICES & APPLICATIONS

(VEDA 2026)

FINAL ANNOUNCEMENT

December 20–22, 2026 **BITS Pilani, Hyderabad Campus**

LAMBDA, Department of EEE, BITS-Pilani, Hyderabad Campus jointly with the Vacuum Electronic Devices & Applications Society (VEDA-S), Bangalore, is organizing the 21st National Conference on Emerging Trends in Vacuum Electronic Devices & Applications (VEDA-2026), during December 20–22, 2026. The conference aims to provide a networking platform for researchers and professionals from R&D, academia, industry, and students to share their work & experience in the broad area of vacuum electronic devices and applications.

The first day of the conference will cover a comprehensive tutorial on design and development aspects of vacuum electronic devices and systems. The subsequent two days will feature plenary sessions, invited talks by renowned experts, and presentations of contributed papers, addressing a wide range of topics in vacuum electronic devices and their applications.

Researchers are invited to submit their work on the following subjects in vacuum electronic devices and applications.

CALL FOR PAPERS

Laboratory for Antenna, Microwave and e-Beam Devices & Applications (LAMBDA), Dept. of EEE, BITS-Pilani, Hyderabad and the Vacuum Electronic Devices & Applications Society (VEDAS, Bengaluru) jointly present the 21st National Conference on Emerging Trends in Vacuum Electronic Devices & Applications (VEDA-2026) from December 20–22, 2026. This annual conference serves as a premier networking platform that brings together researchers, scientists, academicians, industry professionals, and students to exchange ideas and showcase the latest advances in vacuum electronics and allied technologies.

SUBJECT TRACKS

- Linear Beam Devices
- Cross Field Devices
- HPM
- mm-Wave and THz Devices
- Gyro Devices
- Cathode, Vacuum and Fabrication Techniques
- CAD: Optimisation and AI/ML
- Pulsed power, HV and modulators
- Microwave Components, RF Materials, Plasma Devices etc.
- Microwave Systems and Applications
- Research Scholars Forum

SUBMISSION PROCESS

Stage 1 – Initial Submission

Authors are required to submit a 2-page Extended Abstract through the Microsoft CMT portal. The manuscript should be prepared in the IEEE two-column conference format available on the **IEEE Templates Page**.

All submissions will undergo peer review by the Technical Program Committee.

Stage 2 – Final Submission

Authors of accepted papers shall submit a 2-page Final Extended Abstract, incorporating the reviewers' comments, through the **Microsoft CMT** submission portal.

PRESENTATION

At least one author of each accepted paper must register for the conference and present the paper in person. Online presentations will not be permitted.

CONFERENCE PROCEEDINGS

The final extended abstracts of accepted and presented papers will be included in the conference proceedings.

SPECIAL ISSUE

Authors of selected presented papers would be invited to submit a full-length manuscript for consideration in a Special Issue of **SADHANA** – Academy Proceedings in Engineering Sciences (SCIE and Scopus Indexed) which will follow a standard peer-review process in accordance with the journal's editorial policy.

RESEARCH SCHOLARS FORUM

VEDA 2026 will feature a dedicated Research Scholars Forum, providing PhD scholars an opportunity to present their ongoing research through posters and interact with experts from academia, industry, and research laboratories. Scholars currently pursuing their PhD, or those who have defended their PhD thesis during 2026, are eligible to participate. Participants should submit their abstract through the **Microsoft CMT** submission portal under the subject area "Research Scholars Forum" and are required to prepare and bring their poster for presentation at the conference.

*Please note that one registered author can present a maximum of two papers.

IMPORTANT DATES

Submission of Extended Abstracts	**Oct 15, 2026**
Notification of Acceptance	**Oct 30, 2026**
Camera-ready submission	**Nov 15, 2026**
Last date for registration	**Nov 30, 2026**

IMPORTANT LINKS

Templates Link: https://www.ieee.org/conferences/publishing/templates

Submission Link: https://cmt3.research.microsoft.com/VEDA2026/Submission/Index

Payment Link: https://u.payu.in/Prq7zFSDlT9x

Registration Link: https://forms.gle/8zA9tTv8CKRCVXvR7

REGISTRATION FEE

Industry & R&D Delegates	₹ 9,000
Academia Delegates	₹ 6,000
Student Delegates & Retired Professionals	₹ 1,000
VEDA-S member discount	10%
Early Bird discount (before Oct 30, 2026)	₹ 500

CONFERENCE SECRETARIAT

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GENERAL CHAIR

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CONFERENCE WEBSITE

https://www.bits-pilani.ac.in/hyderabad/veda2026

LAMBDA
Laboratory for Antenna, Microwave and e-Beam Devices and Applications
www.bits-pilani.ac.in/hyderabad/lambda/

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