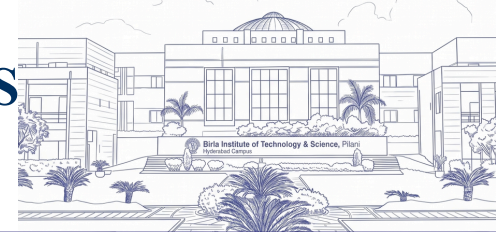


21st NATIONAL CONFERENCE ON EMERGING TRENDS IN VACUUM ELECTRONIC DEVICES & APPLICATIONS (VEDA 2026)



SECOND ANNOUNCEMENT

December 20–22, 2026  **BITS Pilani, Hyderabad Campus**

LAMBDA, Department of EEE, BITS-Pilani, Hyderabad Campus jointly with the Vacuum Electronic Devices & Applications Society (VEDA-S), Bangalore, is organizing the 21st National Conference on Emerging Trends in Vacuum Electronic Devices & Applications (VEDA-2026), during December 20–22, 2026. The conference aims to provide a networking platform for researchers and professionals from R&D, academia, industry, and students to share their work & experience in the broad area of vacuum electronic devices and applications.

The first day of the conference will cover a comprehensive tutorial on design and development aspects of vacuum electronic devices and systems. The subsequent two days will feature plenary sessions, invited talks by renowned experts, and presentations of contributed papers, addressing a wide range of topics in vacuum electronic devices and their applications.

Researchers are invited to submit their work on the following subjects in vacuum electronic devices and applications.

CALL FOR PAPERS

The softcopy of full paper (camera ready), in two-column IEEE format, typeset in 10pt Times New Roman font, single-spaced on A4 size, in 4-page limit, including authors' names and affiliation, abstract, figures, tables, references, in PDF format through email to (veda2026@hyderabad.bits-pilani.ac.in). Papers submitted will be peer-reviewed and published in symposium e-proceedings. Efforts are being made to publish selected peer-review recommended papers in SCI/Scopus indexed publications. Details will be communicated in due course.

Subject Tracks:

- Vacuum Electronic Devices (VEDs): Magnetrons, TWTs, Klystrons, EIK, Gyrotrons, Multi-beam Devices
- High-Power Microwave (HPM) Devices: Viricator, BWO, MILO, Rel. Klystron, etc
- Vacuum-microelectronic mm-wave & THz devices
- Plasma-Assisted Devices and Plasma Thrusters
- Microwave power modules, TR- Modules and Transmitters
- VED based Systems & Applications: Defense, Space, Radar, Telecommunications, Particle Accelerators,
- Microwave systems for Industrial, Scientific & Medical app.
- High-Voltage and Pulsed Power Systems
- Cathodes: Thermionic, Field Emission, and Explosive Emission types
- Computer-Aided Design; Optimization & AI Techniques
- Cold & Hot Microwave Measurements
- Electron Tube Fabrication and Vacuum Technologies
- Advanced RF Materials, Metamaterials and Metasurfaces



IMPORTANT DATES

Submission of Abstracts	Aug 15, 2026
Notification of Acceptance	Sep 15, 2026
Camera-ready Abstract Submission	Oct 15, 2026

MODE OF PAYMENT

Registration fees and sponsorship amounts can be paid securely online via our official payment gateway.

Payment Link: <https://u.payu.in/Prq7zFSDIT9x>

(Note: Please keep a record of your transaction reference number to complete the final registration form).




REGISTRATION FEE

Industry & R&D Delegates	₹ 9,000
Academia Delegates	₹ 6,000
Student Delegates & Retired Professionals	₹ 1,000
VEDA-S member discount	10%
Early Bird discount (before Nov 15, 2026)	₹ 500

CONFERENCE SECRETARIAT


M. Tejashwini / D. Sai Suryanarayana


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CONVENER

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