



Birla Institute of Technology & Science, Pilani

Pilani Campus

Grants Consultancy and Industrial Research (GCIR)

Call for Research Staff Position (JRF)

Date: 20 November 2025

Applications are invited from interested and motivated candidates for the post of Junior Research Fellow (JRF) on a research project sponsored by the Indian Space Research Organization (ISRO), Government of India. The position will be purely on contractual basis as per the following details:

Position	Junior Research Fellow.	Number of Vacancy: 01
Funding Agency	Indian Space and Research Organization (ISRO), Government of India	
Project Title	Development of high-performance self-biased deep UV Photodetectors based on novel 4H-SiC/h-BN heterostructures	
Principal Investigator	Dr. Satyendra Kumar Mourya, Department of Electrical & Electronics Engineering (EEE), BITS Pilani, Pilani Campus	
Project Tenure	24 Months	
Project/Job Description	JRF will perform a simulation for the proposed device structure using TCAD tools. Additionally, he will conduct the fabrication and characterization of the proposed photodetector. JRF will collaborate closely with ISRO scientists to ensure the research is practically relevant and aligned with the mission.	
Qualification	B.E./ B.Tech./ M.E./ M.Tech./ M.Sc., in Physics/ Electronics/ Electrical Engineering/ Electronics and Communication Engineering. Preference to GATE/ NET qualified candidates	
Upper Age Limit	As per the GOI Norms	
Fellowship/Stipend	(i) 37,000/- per month for NET/GATE/GPAT qualified candidates. (ii) 31,000/-per month for non-NET/GATE/GPAT qualified candidates.	
Registration for PhD	Selected candidate may be permitted to register for the PhD program of BITS Pilani, subject to fulfilling the requirements of the PhD qualification process as per the institute norms.	
How to Apply	Email the following documents to: satyendra.mourya@pilani.bits-pilani.ac.in a. CV (Mention past education, technical skills, research experience, publications etc.) b. Cover letter and contact details of at least two academic referees. c. Copy of the NET/GATE/National-level exam score card (if applicable).	
Selection Process	Candidates will be shortlisted based on their merit, experience and as per the requirements of the project. Shortlisted candidates will be informed of the interview, which will be conducted through online mode. Only qualified and suitable candidates will be called for an interview.	

Application Deadline: 25th November 2025

