



## JOB DESCRIPTION

Program Associate (Leffield for Research Excellence in Semiconductor Technology (CRST))         About CREST       BHTS Pillian is committed to becoming the foremost academic insitution for semiconductor Technology (CRST)).         This instative will involve all the institution scampuses, with the Hyderabad campus serving as the primary hub. The CREST aims to produce industry-ready professionals capabilogy (CRST).         Post/ Job Title       Program Associate         Post/ Job Title       Program Associate         Postion &       One Position, Hyderabad Campus         Location       One Position, Hyderabad Campus         Reporting To       Program Manager (CREST)         Position Details       The ideal candidate will support the Program Manager in implementing various technical, executive, and administrative initiatives to implement and sustain the CoE CREST. The person will liaison with various user groups (internal and external), operations and engineering teams, and other stakeholders. The person will also work (closely with all the stakeholders to ensure compliance with all Laboratory policies, and guidelines.         Puties       and         Puties       One Process Equipment operation and baseline monitoring.         New process Equipment acortrol the belio f equipment manufacturers to achieve the desired values of the performance requirement and develop recipes which can evaluate tool performance:         Uteits       and         Compliance bit the thelp of equipment manufacturers to achieve the desired values of the performance parameters.	Pilani   Dubai   Goa   Hyderabad   Mumbai	JOB DESCRIPTION m Associate [Contex for Research Excellence in Semiconductor Technology (CREST)]
research and innovation in India through the establishment of the Center of Excellence in Semiconductor called Center for Research Excellence in Semiconductor Technology (CREST). This initiative will involve all the institution's campuses, with the Hyderabad campus serving as the primary hub. The CREST aims to produce industry-ready professionals capable of excelling 	Program Associate [Center for Research Excellence in Semiconductor Technology (CREST)]	
Position & Location         One Position, Hyderabad Campus           Tenure:         3 years and further extension based on satisfactory performance           Reporting To         Program Manager (CREST)           Position Details         The ideal candidate will support the Program Manager in implementing various technical, executive, and administrative initiatives to implement and sustain the CoE CREST. The person will liaison with various user groups (internal and external), operations and engineering teams, and other stakeholders. The person will also work closely with all the stakeholders to ensure compliance with all Laboratory policies, and guidelines.           Process Equipment operation and baseline monitoring.         • Process Equipment operation, and baseline monitoring.           • New process development.         • Perform set-up, calibration, and minor routine preventative maintenance.           • Identify tool performance requirement and develop recipes which can evaluate tool performance parameters.         • Torobleshoot the tool with the help of equipment manufacturers to achieve the desired values of the performance parameters.           • Torobleshoot the tool with the help of equipment manufacturers to achieve the desired values of the performance parameters.         • Documentation wherever required.           • Baintenance Troubleshooting Support to reduce tool downtime         • Software: modelling and data interpretation.           • Equipment operational and handling chemical in the labs         • Good exposure on semiconductor device process like deposition, growth, lithography, wet etching and analytical characterization      <	About CREST	research and innovation in India through the establishment of the Center of Excellence in Semiconductor called Center for Research Excellence in Semiconductor Technology (CREST). This initiative will involve all the institution's campuses, with the Hyderabad campus serving as the primary hub. The CREST aims to produce industry-ready professionals capable of excelling in the semiconductor sector, achieving innovative breakthroughs and intellectual outputs, and establishing a sustainable ecosystem for students, researchers, and startups, complete with the
LocationImage: CRESTReporting ToProgram Manager (CREST)Position DetailsThe ideal candidate will support the Program Manager in implementing various technical, executive, and administrative initiatives to implement and sustain the CoE CREST. The person will laison with various user groups (internal and external), operations and engineering teams, and other stakeholders. The person will also work closely with all the stakeholders to ensure compliance with all Laboratory policies, and guidelines.Puties• Process Equipment operation and baseline monitoring. • New process development. • Perform set-up, calibration, and minor routine preventative maintenance. • Identify tool performance requirement and develop recipes which can evaluate tool performance parameters. • Establish, monitor, and control the base line recipes. • General Lab and Facility Management: monitor cleaning/usage procedures, safety procedures. • Documentation wherever required. • Maintenance Troubleshooting Support to reduce tool downtime • Software: modelling and data interpretation. • Equipment operational and handing chemical in the labs • Good exposure on semiconductor device process like deposition, growth, lithography, wet etching and analytical characterizationEducational QualificationM.Tech. with a focus on Semiconductor devices / materialsDesirableExperience to develop and work in Semiconductor Clean room	Post/ Job Title	Program Associate
Reporting To         Program Manager (CREST)           Position Details         The ideal candidate will support the Program Manager in implementing various technical, executive, and administrative initiatives to implement and sustain the CoE CREST. The person will liaison with various user groups (internal and external), operations and engineering teams, and other stakeholders. The person will also work closely with all the stakeholders to ensure compliance with all Laboratory policies, and guidelines. <ul> <li>Process Equipment operation and baseline monitoring.</li> <li>New process development and optimization.</li> <li>Work with lab users to conduct tests and experiments that support process improvement or process development.</li> <li>Perform set-up, calibration, and minor routine preventative maintenance.</li> <li>Identify tool performance requirement and develop recipes which can evaluate tool performance parameters.</li> <li>Troubleshoot the tool with the help of equipment manufacturers to achieve the desired values of the performance parameters.</li> <li>Establish, monitor, and control the base line recipes.</li> <li>General Lab and Facility Management: monitor cleaning/usage procedures, safety procedures.</li> <li>Documentation wherever required.</li> <li>Maintenance Troubleshooting Support to reduce tool downtime</li> <li>Software: modelling and data interpretation.</li> <li>Equipment operational and handling chemical in the labs</li> <li>Good exposure on semiconductor device process like deposition, growth, lithography, wet etching and analytical characterization</li> </ul> <li>Educational Qualification</li> <li>M.Tech. with a focus on Semiconductor devi</li>		One Position, Hyderabad Campus
Position Details       The ideal candidate will support the Program Manager in implementing various technical, executive, and administrative initiatives to implement and sustain the CoE CREST. The person will liaison with various user groups (internal and external), operations and engineering teams, and other stakeholders. The person will also work closely with all the stakeholders to ensure compliance with all Laboratory policies, and guidelines. <ul> <li>Process Equipment operation and baseline monitoring.</li> <li>New process development and optimization.</li> <li>Work with lab users to conduct tests and experiments that support process improvement or process development.</li> <li>Perform set-up, calibration, and minor routine preventative maintenance.</li> <li>Identify tool performance requirement and develop recipes which can evaluate tool performance parameters.</li> <li>Troubleshoot the tool with the help of equipment manufacturers to achieve the desired values of the performance parameters.</li> <li>Establish, monitor, and control the base line recipes.</li> <li>General Lab and Facility Management: monitor cleaning/usage procedures, safety procedures.</li> <li>Documentation wherever required.</li> <li>Maintenance Troubleshooting Support to reduce tool downtime</li> <li>Software: modelling and data interpretation.</li> <li>Equipment operational and handling chemical in the labs</li> <li>Good exposure on semiconductor devices / materials</li> </ul> Desirable     Experience to develop and work in Semiconductor Clean room	Tenure:	3 years and further extension based on satisfactory performance
executive, and administrative initiatives to implement and sustain the CoE CREST. The person will liaison with various user groups (internal and external), operations and engineering teams, and other stakeholders. The person will also work closely with all the stakeholders to ensure compliance with all Laboratory policies, and guidelines.• Process Equipment operation and baseline monitoring. • New process development and optimization. • Work with lab users to conduct tests and experiments that support process improvement or process development. • Perform set-up, calibration, and minor routine preventative maintenance. • Identify tool performance requirement and develop recipes which can evaluate tool performance parameters. • Troubleshoot the tool with the help of equipment manufacturers to achieve the desired values of the performance parameters. • Establish, monitor, and control the base line recipes. • General Lab and Facility Management: monitor cleaning/usage procedures, safety procedures. • Documentation wherever required. • Maintenance Troubleshooting Support to reduce tool downtime • Software: modelling and data interpretation. • Equipment operational and handling chemical in the labs • Good exposure on semiconductor device process like deposition, growth, lithography, wet etching and analytical characterizationEducational QualificationM.Tech. with a focus on Semiconductor devices / materialsDesirableExperience to develop and work in Semiconductor Clean roomRemunerationRs 9 Lakhs annual CTC (based on experience including target-based performance incentives).	Reporting To	Program Manager (CREST)
<ul> <li>New process development and optimization.</li> <li>Work with lab users to conduct tests and experiments that support process improvement or process development.</li> <li>Perform set-up, calibration, and minor routine preventative maintenance.</li> <li>Identify tool performance requirement and develop recipes which can evaluate tool performance parameters.</li> <li>Troubleshoot the tool with the help of equipment manufacturers to achieve the desired values of the performance parameters.</li> <li>Establish, monitor, and control the base line recipes.</li> <li>General Lab and Facility Management: monitor cleaning/usage procedures, safety procedures.</li> <li>Documentation wherever required.</li> <li>Maintenance Troubleshooting Support to reduce tool downtime</li> <li>Software: modelling and data interpretation.</li> <li>Equipment operational and handling chemical in the labs</li> <li>Good exposure on semiconductor device process like deposition, growth, lithography, wet etching and analytical characterization</li> <li>Educational Qualification</li> <li>M.Tech. with a focus on Semiconductor devices / materials</li> <li>Experience to develop and work in Semiconductor Clean room</li> <li>Remuneration</li> <li>Res 9 Lakhs annual CTC (based on experience including target-based performance incentives).</li> </ul>	Position Details	executive, and administrative initiatives to implement and sustain the CoE CREST. The person will liaison with various user groups (internal and external), operations and engineering teams, and other stakeholders. The person will also work closely with all the stakeholders to ensure
Qualification       M. Lech. with a focus on Semiconductor devices / materials         Desirable       Experience to develop and work in Semiconductor Clean room         Remuneration       Rs 9 Lakhs annual CTC (based on experience including target-based performance incentives).		<ul> <li>New process development and optimization.</li> <li>Work with lab users to conduct tests and experiments that support process improvement or process development.</li> <li>Perform set-up, calibration, and minor routine preventative maintenance.</li> <li>Identify tool performance requirement and develop recipes which can evaluate tool performance parameters.</li> <li>Troubleshoot the tool with the help of equipment manufacturers to achieve the desired values of the performance parameters.</li> <li>Establish, monitor, and control the base line recipes.</li> <li>General Lab and Facility Management: monitor cleaning/usage procedures, safety procedures.</li> <li>Documentation wherever required.</li> <li>Maintenance Troubleshooting Support to reduce tool downtime</li> <li>Software: modelling and data interpretation.</li> <li>Equipment operational and handling chemical in the labs</li> <li>Good exposure on semiconductor device process like deposition, growth, lithography, wet</li> </ul>
Remuneration       Rs 9 Lakhs annual CTC (based on experience including target-based performance incentives).		M.Tech. with a focus on Semiconductor devices / materials
	Desirable	Experience to develop and work in Semiconductor Clean room
Application         Please complete this form https://forms.gle/AemgYN6zoADPhqgi9	Remuneration	Rs 9 Lakhs annual CTC (based on experience including target-based performance incentives).
	Application	Please complete this form https://forms.gle/AemgYN6zoADPhqgi9

