



BITS Pilani

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Research & Innovation

ONE PhD Scholar Position

Mathematical Modeling and Optimization of Multi-Sized Discrete Cosine Transform Architectures for High Efficiency Video Coding

Hyderabad Campus

Joining: At the earliest

Date: 22 October 2025

Applications are invited for **ONE** position of PhD Scholar on project, “**Mathematical Modeling and Optimization of Multi-Sized Discrete Cosine Transform Architectures for High Efficiency Video Coding**” under the supervision of **Prof. Manish Kumar** and **Prof. Sumit K Chatterjee**

Deserving candidates check the eligibility criteria and qualification process of the PhD program of BITS Pilani (<http://www.bitsadmission.com/phmain.aspx>).

Scope of work	Essential Qualification	Desirable Qualification
Mathematical Modeling and Optimization of Multi-Sized Discrete Cosine Transform Architectures for High Efficiency Video Coding	M.Sc. in Mathematics/MTech in Electronics and Communication Engineering/ Electrical Engineering with minimum of 60% aggregate	Engineering Background with prior knowledge on VLSI design and mathematical modelling with good knowledge of computer programming (Python)

Note: NET/GATE qualifications are not mandatory for the present positions.

Fellowship: ₹37,000 - ₹42,000 per month (based on the year of PhD and performance)

Duration: As per BITS Pilani norms (<http://www.bitsadmission.com/phmain.aspx>)

Place of work: BITS Pilani, Hyderabad Campus, Hyderabad.

Application process: Please apply with **CV and Cover letter** (showing alignment and justification with the roles/responsibilities/requirements) using this form

- Google form link: <https://forms.gle/X5eRAM3tJHF5KUqx9>
- Deadline: [3 November 2025 or until a suitable candidate is selected.]

Preliminary shortlisting will be based on resume and telephonic/audio-visual interview within a week of last date of application. For final interview, the candidate will be informed through e-mail for interview. No TA/DA will be provided in case of personal interview. For more details, please contact:

1. Prof. Manish Kumar,

Associate Professor

Department of Mathematics

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2. Prof. Sumit K Chatterjee,

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