



Birla Institute of Technology & Science, Pilani
K K Birla Goa Campus

Junior Research Fellow Position under ANRF Funded Project

Applications are invited for the position of **Junior Research Fellow** in the **Department of Electrical and Electronics Engineering** at BITS Pilani, K. K. Birla Goa Campus, Goa under the ANRF-PMECRG project.

This position is applicable only to **PhD aspirants**. Candidates should check the eligibility criteria and qualification process of the PhD program of BITS Pilani(<http://www.bitsadmission.com/phmain.aspx>).

Supervisor: Dr. Tarakanath Kobaku (BITS Pilani Goa Campus)

For more details, please see profile (s): <https://www.bits-pilani.ac.in/goa/tarakanath-k/>

Number of positions: 1 at BITS Pilani K K Birla Goa Campus. (Immediate joining within a month of interview if selected).

Broad Area of Research: Power Electronics, Fuel cell and Control Systems

Scope of Work: The research fellow will work on the modelling, control, and experimental validation of Fuel cell driven power converter systems. The project focuses on developing robust control algorithms to address voltage regulation, instability, sensor noise, and converter uncertainties. The student will gain experience in advanced power electronics, real-time control implementation, and hardware validation using Fuel cell emulators, power electronic converters.

Duration: As per BITS Pilani norms (<http://www.bitsadmission.com/phmain.aspx>)

Essential Qualification: Post Graduate or Graduate degree in EEE/ Power Electronics/Control Systems or equivalent discipline from a recognized University.

Desirable Qualification: Strong background in Power Electronics, Control Systems design. Knowledge of Matlab/Simulink, programming TMS320FXXXX , and PCB design is added advantage.

Accommodation: Hostel accommodation will be provided subject to availability and will be governed by the rules and regulations of the Institute prevailing at the time of joining of the candidate.

Fellowship Amount (Rs.): ₹37,000 - ₹42,000 per month (based on the year of Ph D and performance)

How to Apply: Please submit your **CV and Cover letter** (showing alignment and justification with the roles/responsibilities/requirements) to tarakanathk@goa.bits-pilani.ac.in

Last Date: 31st July 2026

