



**Birla Institute of Technology & Science, Pilani**

**Pilani Campus**

Grants Consultancy and Industrial Research (GCIR)

**CALL FOR RESEARCH STAFF (JRF) POSITION**

**Date: 15 February 2024**

<b>About:</b> Applications are invited from interested and motivated candidates for the post of Junior Research Fellow (JRF) in a time bound research project sponsored by the <b>TIH Foundation for IoT &amp; IoE , IIT Bombay</b> ( F. No. TIH-IoT/2023-12/TDP6/Core/SI-002) for a temporary period purely on contractual basis as per the following details:	
<b>Position:</b>	Junior Research Fellow (JRF)
<b>Number of Vacancy:</b>	01
<b>Project Title:</b>	Multisensor Fusion Toolbox for Edge-AI Computing to Support IoT and MIIoTdriven Building Automation and Management Applications
<b>Funding Agency:</b>	<b>TIH Foundation for IoT &amp; IoE , IIT Bombay</b>
<b>Principal Investigator (PI):</b>	Dr. Meetha V Shenoy, Department of Electrical and Electronics Engineering
<b>Project Tenure:</b>	1 Year,10 months
<b>Job Description:</b>	The candidates will carry out the task of designing and developing a modular multisensor toolbox for analyzing the possibility of multisensor fusion at the edgefor IoT and MIIoT driven intelligent building automation and management applications. The toolbox developed will provide the designers and practitioners insights into choosing sensors, deployment of IoT nodes, the feasibility of edge computing and/or edge AI based multisensor fusion solutions, the type of sensorfusion techniques that can be performed, the cost performance tradeoff of the fusion techniques, or provide insights into the quality of the edge computing solution. The candidates will participate in the design of the architecture of the toolbox, development of the toolbox, development of IoT/MIIoT based testbeds, interface with the industry collaborator, etc.
<b>Qualification:</b>	M.E. / M. Tech. in Electronics/ Communication Engineering/Embedded Systems/Computer Science/Software Systems/Information Technology/ /Microelectronics/VLSI/ or M.Sc. in Electronics or equivalent degree. B.E/B.Tech in Computer Science/Software Systems/Information Technology may also be considered if found suitable. <b>Desirable:</b> Good understanding IoT networks, IoT communication protocols or Signal Processing techniques. Prior experience in sensor fusion techniques, IoT Network Simulators. Basic understanding of AI/ML techniques, C/C++/Python programming, MATLAB. Prior experience in IoT Application layer protocols, Front end design (UI/UX), Python based GUI Frameworks, SQL or NoSQL databases, Restful API Development, Basic understanding of AI/ML techniques.
<b>Fellowship:</b>	37,000 P.M
<b>Registration for PhD.:</b>	Selected candidates may be permitted to register for the PhD program of BITS Pilani subject to fulfilling the requirements of the PhD qualification process as per the institute norms.



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**How to Apply:**

Applications along with an updated CV should be sent through the mail to [meetha.shenoy@pilani.bits-pilani.ac.in](mailto:meetha.shenoy@pilani.bits-pilani.ac.in). Mention the subject of the email as “Application for the post of Junior Research Fellow (JRF)”. Shortlisted candidates will be informed of the interview, which will be conducted through online mode. Possession of minimum qualification does not guarantee an invitation to the interview. Candidates will be shortlisted based on their merit and as per the project's requirements. Please note that only qualified and suitable candidates will be called for an interview.

**Application Deadline: 14 March 2024**

**Note: Since this is an immediate requirement, interviews will be conducted in batches as and when a sufficient number of applications are received. Hence, you are requested to apply as soon as possible without waiting for the deadline.**

**Contact Email: [meetha.shenoy@pilani.bits-pilani.ac.in](mailto:meetha.shenoy@pilani.bits-pilani.ac.in)**

