Call for Research Staff Position (JRF)   Date: 01 January 2026

About: Applications are invited from interested and motivated candidates for the post of Junior Research Fellow (JRF) on the research project sponsored by the **Ministry of Electronics and Information Technology (MeitY)**. The position will be purely on contractual basis as per the following details:

Position	Junior Research Fellow	Number of Vacancy: 02
Funding Agency	**Ministry of Electronics and Information Technology (MeitY)**	
Project Title	Design and Development of an AI/ML-Driven Toolkit for Automated Cybersecurity Audits and Compliance Framework	
Principal Investigator	Dr. Vishal Gupta, Department of Computer Science and Information Systems, BITS Pilani, Pilani Campus	
Project Tenure	2 Years	
Project/Job Description	The selected JRF will work on a R&D project jointly undertaken by BITS Pilani and IIT Delhi, focusing on the design and development of an AI/ML-driven cybersecurity audit and compliance framework for government and critical infrastructure systems. The JRF will be involved in research and development of automated cybersecurity auditing tools, continuous monitoring mechanisms, and AI/ML-based risk assessment and vulnerability analysis. Responsibilities include implementing audit modules for operating systems, networks, cloud and endpoints, integrating compliance frameworks (such as ISO 27001, NIST, CIS benchmarks), developing ML models for anomaly detection and risk scoring, and supporting prototype development, experimentation, documentation, and research publications.	
Qualification	M.E./M.Tech or equivalent degree in Computer Science / Computer Engineering / Information Security / Cybersecurity / Artificial Intelligence / Data Science / Electronics / IT or a closely related discipline, with at least 60% marks or equivalent CGPA. **OR** B.E./B.Tech/MCA in the above disciplines with valid GATE score (or appear for written test as per institute norms).	
Upper Age Limit	28 years as of the date of the interview. Age relaxation is allowed as per government rule.	
Fellowship/Stipend	Rs. 37,000/- per month	
Registration for PhD	Selected candidates may be permitted to register for the PhD program of BITS Pilani, subject to fulfilling the requirements of the PhD qualification process as per the institute norms.	
How to Apply	**Email the following documents to**: vishalgupta@pilani.bits-pilani.ac.in a. **CV** (Mention past education, technical skills, research experience, publications, etc.) b. **Cover letter** and contact details of at least two academic referees. c. Copy of the NET/GATE/National-level exam score card (if applicable).	

Selection Process	Candidates will be shortlisted based on their merit, experience, and as per the requirements of the project. Shortlisted candidates will be informed of the interview, which will be conducted through online mode. **Only qualified and suitable candidates will be called for an interview.**

Application Deadline: 10th January, 2026