# Birla Institute of Technology & Science, Pilani
Pilani Campus

## JOB DESCRIPTION

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| **About BITS Pilani** | **Birla Institute of Technology and Science (BITS Pilani)**, established in 1964 as a deemed university under Section 3 of the UGC Act, is a renowned science and technology institution with campuses in Pilani, Dubai, Goa and Hyderabad. Its Work Integrated Learning Programmes (WILP), established in 1979, offer B.Sc., B.Tech., M.Tech., M.Sc., Diploma and Certificate programmes for working professionals. |
| **Industry/Service** | Higher Education |
| **Post/Job Title** | Senior Instructor / Instructor (Full Time) |
| **Job Type** | The tenure will be for two years at a time, with further extensions subject to satisfactory performance – **(Senior Instructor)**<br>Appointment for one year at a time, extendable up to four years, subject to satisfactory performance – **(Instructor)** |
| **Reporting to** | Prof. Paramesw Chidamparam, Head – Engineering Group |
| **Works closely with** | WILP faculty members and students, and software and hardware vendors. |
| **Positions & Location** | One position at Hyderabad (Off Campus); travel as required. |
| **Principal Accountabilities & Responsibilities** | • Deliver lab sessions for M.Tech and B.Tech VLSI-related courses, bridging class lectures and hands-on execution or simulation.<br>• Develop lab manuals and keep their content up to date; support faculty in designing new manuals, prototyping circuit benchmarks and upgrading infrastructure.<br>• Deploy lab exercises to students per syllabus timelines; assist faculty in practical sessions, explaining concepts and guiding experimental workflows.<br>• Keep EDA tools updated to the latest versions, troubleshoot server issues, and track licenses for timely renewal.<br>• Maintain an up-to-date stock ledger of ICs, development boards and test units; oversee device check-outs, preventive servicing, calibration schedules and external AMCs.<br>• Maintain audit-ready logs, including stock registers, usage records and equipment logs.<br>• Resolve tool queries promptly and enforce lab safety protocols, including ESD protection for sensitive VLSI chips. |
| **Qualification & Profile** | • M.E/M.Tech in ECE with strong hands-on VLSI/Microelectronics/Semiconductor/MEMS background (strong foundation in VLSI lab setups and EDA tools) **Senior Instructor**<br>• B.E/B.Tech in ECE with strong hands-on VLSI/Microelectronics/Semiconductor/MEMS background (strong foundation in VLSI lab setups and EDA tools) **Instructor**<br>• Minimum 1-3 years of hands-on experience with EDA tools and server management in industry or an academic institution.<br>• Experience conducting lab tutorials or administering labs in universities, colleges, EdTech/training, test-prep/assessment or skill-development organizations. |
| **Compensation** | Up to ₹11 lakh per annum, commensurate with experience **(Senior Instructor)**<br>Up to ₹09 lakh per annum, commensurate with experience **(Instructor)** |
| **Apply Link** | Candidates should submit their resumes through the following Google Form:<br>https://forms.gle/JB1ybtT3PbV8Dq9Z9 |