



Advertisement for the post of IP and Technology Officer DST-BITS Technology Enabling Centre, BITS Pilani

About BITS, Pilani	Birla Institute of Technology & Science Pilani (BITS Pilani) is an Institution declared as a deemed University under Sec. 3 of the UGC Act in 1964, and is a renowned Science and Technology institute. It is located in Pilani, Rajasthan, India.
About Program	BITS Pilani has set up a Technology Enabling Center (TEC), a program under the TDT Division of the Department of Science and Technology (DST), GoI. The objective of TEC will be to create an enabling Ecosystem for Technology Development, processes, and support systems for regional academic institutes, MSMEs, industry, and start-ups, and to provide a platform to network researchers with other institutes, National laboratories, and industry. BITS-TEC hub will act as a catalyst and sensitize the regional stakeholders to the best practices of Research and intellectual management through structured support in policy implementation, IP creation and protection, technology improvement, product development and validation, and technology transfer.
Post/Job Title	IP and Technology Officer
Project Duration	Till March 2028
Job Type	Temporary; Co-terminus with the project
Reporting to	Coordinator/Co-coordinator of BITS Pilani TEC or as assigned by the coordinator
Will also work very closely with	The Chief Operating Officer (COO) and the TEC team
Job Location (No.)	Pilani Campus, BITS Pilani (One)
Principal Accountabilities & Responsibilities	<ul style="list-style-type: none"> • Skilling and Training: Achieving the overall goals and annual targets set up by the TEC for training and skill development of regional stakeholders. • Assist internal and external innovators in IP generation and protection, leading to technology commercialization. • Identifying external entities with an interest in faculty research area(s), and developing research partnerships. • Develop and maintain strong working relationships with regional Government departments, Higher Educational Institutions, and Research centers to disseminate information regarding technology transfer and intellectual property; Monitor and advise on the application of intellectual property policy and relevant regulations. • Other tasks may include work given by the coordinator as and when necessary.
Other Skill and Ability Requirements	<ul style="list-style-type: none"> • IP & Tech Manager will perform various activities related to IP Management: Patent/Trade Mark/Design /Copyright/other IP-related Drafting and Filing / IP-related prosecution advisory, analysis, landscaping, IP mapping, prior art search, etc.



	<ul style="list-style-type: none">• Technology commercialization: All aspects related to technology commercialization, term sheets, license agreements, NDA, MTA, technology scouting and match-making, and other related activities.• Prior demonstrated experience in IP management, IP filing, and documentation. licensing, technology transfer, related policies, and commercialization of research, contracts, and agreements with industry, association, and academic institutes.• Demonstrated knowledge of the technology transfer life cycle, including the ability to guide academic faculties, MSMEs, start-ups, and innovators towards commercialization goals.
Qualification and Personal Profile	<p>Essential:</p> <ol style="list-style-type: none">1. PhD in Science or Master's degree in engineering/ technology from a recognized university from a recognized university <p>Desirable:</p> <ul style="list-style-type: none">• Relevant experience in IP and Technology Management.• A registered Patent agent with a technical degree. A good practical understanding of IP filings, documentation, and management
Upper age limit	40 Years
Annual Salary	As per DST norms (Rs. 56000/- Month + HRA as per institute norms with 5% increment after 2 years)

➤ Application process:

- Eligible candidates may please apply by 27th Sept 2025 with a detailed CV using the online form available at: <https://forms.gle/G3rgCW2hcDaLm2m3A>
- For queries, contact at the following email ID: tec.bits@pilani.bits-pilani.ac.in.

NOTE:

1. Positions mentioned are purely temporary and for the duration of the project. Renewal of the position is subject to a satisfactory annual performance review. If performance is found “Not satisfactory”, the position can be terminated with one month of prior notice.
2. No TA/DA will be paid for attending the interview.