



BITS Pilani
Pilani Campus

Admission to
Ph.D. Program
1st semester, 2024-25



PhD at Department of Electrical & Electronics Engineering

Ph.D. Schemes Offered

Full-Time (FT)

- FT scholars will reside on campus
- Are eligible for institute fellowship
- Will be involved in academic assignments

Part-time (PT)

- PT scholars need to submit NOC from the employer
- Will work from their workplace on problems relevant to the EEE department
- Will have to visit the campus 2-3 times a semester or as required by the competent authority

Ph.D. Aspirant

- To fulfil the PhD aspiration of individuals of high professional standing and proven experience from Industries, R & D organizations and Government organizations
- Will work from their workplace on problems relevant to their work environment
- The Admission notification remains open throughout the year (<https://www.bitsadmission.com/phdmain.aspx>)

Eligibility for Applying

Minimum academic record of 60 % marks or 6/10 CGPA in one of the following,

- M.E/ M.Tech. in relevant area (both FT and PT schemes)
- M. Sc. in Electronics / Electronics Science / Physics, etc.

Selection

Selection comprises of written test and/or interview. Candidates with Higher Degree (M.E. / M.Tech.) as the highest qualification or those with National Fellowships like CSIR / DBT/UGC are exempted from the written test. Selected candidates will undergo coursework with minimum performance requirements.

update from <https://bitsadmission.com/bitsphweb/Index.aspx>

Important dates

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| Admission Portal Open | 1 st March 2024 |
| Last Date for filling application form online | 29 th April 2024 |
| Declaration of shortlisted candidates for written test and interview | 6 th May 2024 |
| Reporting at the BITS Pilani Campus | 25 th July 2024 |
| Orientation | 26 th and 27 th July 2024 |
| Course Registration | 1 st August 2024 |
| Beginning of classwork | 2 nd August 2024 |

Major Research Areas

- Instrumentation & Control
- Communication Systems & Networks
- RF, Microwave, Antenna Design & Wireless system
- Power Systems, Electrical Energy, Renewable Energy & Smart Grids
- Power Electronics & Drives
- IoT and Embedded Systems
- Micro / Nano Electronics & VLSI
- Electronic Materials, Devices & Technology
- Digital Signal Processing

Major Research Labs

- Intelligent Instruments & Control Lab
- Nanoelectronics Lab
- Reconfigurable Computing Lab
- Sensor Networks and IoT Lab
- Microelectronics/ VLSI Design Lab (O-Lab)
- Embedded Controller Application Centre
- Power Electronics Lab
- Optical and Wireless Communication Lab
- Communication Engineering Lab
- Flexible Electronics Lab

Institute Fellowship and Fee structure

FT students will receive a monthly fellowship of ₹34,000-42,000 per month. Additionally, annual contingency and travel grants to attend workshops and conferences will also be provided.

For the fee structure and other details, please visit

https://bitsadmission.com/bitsphweb/downloads/PhD_Fee_Structure_2023_24.pdf?202425

Components of the Ph.D. Course

- Course work 1/2 semesters
- Qualifying examination (2 semesters max.)
- Research proposal (2 semesters max.)
- Thesis units (4 semesters)
- Research publication requirements

FOR MORE DETAILS visit <https://www.bitsadmission.com/phdmain.aspx>

APPLY HERE

<https://bitsadmission.com/phd2024sem1xbits/Default>

CONTACT

Department of Electrical & Electronics Engineering

Phone: +91-1596-25-5233, +91-1596-25-5280

Email: hod.eee@pilani.bits-pilani.ac.in

<https://www.bitspilani.ac.in/pilani/>

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