



BITS Pilani
Pilani Campus

Admission to
Ph.D. Program
2nd semester, 2023-24



PhD at Department of Electrical & Electronics Engineering

Ph.D. Schemes Offered

Full-Time (FT)

- FT scholars will reside on campus
- Are eligible for institute fellowship
- Will be involved in academic assignments

Part-time (PT)

- PT scholars need to submit NOC from the employer
- Will work from their workplace on problems relevant to the EEE department
- Will have to visit the campus 2-3 times a semester or as required by the competent authority

Ph.D. Aspirant

- To fulfil the PhD aspiration of individuals of high professional standing and proven experience from Industries, R & D organizations and Government organizations
- Will work from their workplace on problems relevant to their work environment
- The Admission notification remains open throughout the year (<https://www.bitsadmission.com/phdmain.aspx>)

Eligibility for Applying

Minimum academic record of 60 % marks or 6/10 CGPA in one of the following,

- M.E/ M.Tech. in relevant area (both FT and PT schemes)
- M. Sc. in Electronics / Electronics Science / Physics, etc.

Selection

Selection comprises of written test and/or interview. Candidates with Higher Degree (M.E. / M.Tech.) as the highest qualification or those with National Fellowships like CSIR / DBT/UGC are exempted from the written test. Selected candidates with first degree (MSc) will undergo coursework for two semesters with minimum performance requirements.

Important dates

Admission Portal Open	7-October-2023
Last Date for filling application form online	25-November-2023
Declaration of shortlisted candidates (on BITS admission website)	15-December-2023
Date of Admission Test/Interview	3-January-2024
Result Notification	6-January-2024
Deadline for Fee Payment	15-January-2024
Student Reporting	18-January-2024
Student Registration	18-January-2024

Major Research Areas

- Instrumentation & Control
- Communication Systems & Networks
- RF, Microwave, Antenna Design & Wireless system
- Power Systems, Electrical Energy, Renewable Energy & Smart Grids
- Power Electronics & Drives
- IoT and Embedded Systems
- Micro / Nano Electronics & VLSI
- Electronic Materials, Devices & Technology
- Digital Signal Processing

Major Research Labs

- Intelligent Instruments & Control Lab
- Nanoelectronics Lab
- Reconfigurable Computing Lab
- Sensor Networks and IoT Lab
- Microelectronics/ VLSI Design Lab (O-Lab)
- Embedded Controller Application Centre
- Power Electronics Lab
- Optical and Wireless Communication Lab
- Communication Engineering Lab
- Flexible Electronics Lab

Institute Fellowship and Fee structure

FT students will receive a monthly fellowship of ₹34,000-42,000 per month. Additionally, annual contingency and travel grants to attend workshops and conferences will also be provided.

For the fee structure and other details, please visit

https://www.bitsadmission.com/2023/phd/PhD_Fee_Structure_2023_24.pdf?2022

Components of the Ph.D. Course

- Course work (2 semesters, if applicable)
- Qualifying examination (2 semesters max.)
- Research proposal (2 semesters max.)
- Thesis units (4 semesters)
- Research publication requirements

FOR MORE DETAILS visit <https://www.bitsadmission.com/phdmain.aspx>

APPLY HERE

<https://bitsat.cbexams.com/phd2023xsem2apply/OnlineReg.aspx>

CONTACT

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ElectricalElectronicsInstrumentation/Research

