



JUNIOR RESEARCH FELLOW (JRF) POSITION

DST-INAE India-Taiwan funded research project

Date: 03 Sep 2026

About: Applications are invited from interested and motivated candidates for one Junior Research Fellow (JRF) position in the Department of Electrical & Electronics Engineering, BITS Pilani, K. K. Birla Goa Campus, under the DST-INAE India-Taiwan Programme of Cooperation in Science & Technology.

Position	JUNIOR RESEARCH FELLOW (JRF)	Number of Vacancy: 01
Funding Agency	Department of Science & Technology (DST), Government of India / Indian National Academy of Engineering (INAЕ) - India-Taiwan Programme of Cooperation in Science & Technology	
Project Title	3D Printed Tacky Liquid Metal-Ionogel Hybrids for Bioelectronic Interfaces	
Indian PI	Prof. Tushar Sakorikar, Department of Electrical & Electronics Engineering, BITS Pilani, K. K. Birla Goa Campus	
Taiwanese PI	Prof. Sheng-Sheng Yu, Department of Chemical Engineering, National Cheng Kung University (NCKU), Taiwan	
Project Tenure	3 years	
Job Description	The JRF will work on the development of soft, conductive, and adhesive polymeric materials for skin-interfaced bioelectronics. The work will broadly involve soft-material/polymer formulation, functional liquid-metal composites, rheology and extrusion-based 3D printing, electrical transport/impedance and electromechanical characterization, and evaluation of wearable electrodes for electrophysiological recordings. The position includes at least one funded research visit to NCKU, Taiwan. Expected joining: September 2026 / immediately after completion of the selection process.	
Qualification	Qualifications: B.E./B.Tech. or M.E./M.Tech. or M.Sc. in Chemistry/Polymer Science, Materials Science/Engineering, Chemical Engineering, Physics, Nanotechnology, Biomedical Engineering, Electrical/Electronics Engineering, Instrumentation, or closely related areas. Desirable: Cleared CSIR-UGC NET (including lectureship/Assistant Professorship), GATE, or another applicable national-level selection process conducted by Central Government departments/agencies/institutions, as per prevailing project norms. Preferred research experience: Hands-on experimental experience in polymer synthesis/formulation, gels/soft materials, functional or conductive composites, rheology, 3D-printable materials/additive manufacturing, or related areas. Exposure to electrical transport or impedance characterization, bioelectronics, flexible sensors, electrophysiology, or related device characterization would be an added advantage. Prior experience with liquid metals is welcome but not essential. Candidates with a strong materials-development/formulation background will be particularly preferred.	
Upper Age Limit	As per applicable project / Government of India norms.	
Fellowship/Stipend	Rs. 37,000 per month + 10% HRA for the 1st and 2nd years as JRF; Rs. 42,000 per month + 10% HRA for the 3rd year as SRF, subject to upgradation/assessment and applicable project norms.	
Registration for PhD	The selected candidate will be encouraged to apply for the PhD program at BITS Pilani, K. K. Birla Goa Campus, subject to fulfilling Institute admission requirements and applicable PhD program rules.	



Birla Institute of Technology & Science, Pilani

K K Birla Goa Campus

How to Apply	Interested candidates should email (i) latest CV and (ii) a brief note (max. 200 words) describing one relevant experimental/material/device project and the candidate's specific contribution, to tushars@goa.bits-pilani.ac.in by 7th September 2026. Only shortlisted candidates will be contacted for an online/personal interview. No TA/DA will be paid for attending the interview.
Selection Process	Candidates will be shortlisted based on merit, relevant research experience, and the project's requirements. Shortlisted candidates will be informed of the interview, which may be conducted online/offline. Mere possession of the minimum qualification does not guarantee an interview invitation.

Application Deadline: 7th September 2026

For any queries, please contact me. Those who have already applied for the same project need not reapply.

Prof. Tushar Sakorikar

Department of Electrical and Electronics Engineering

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tushars@goa.bits-pilani.ac.in