Birla Institute of Technology & Science, Pilani
Hyderabad Campus

BITS Pilani- IISc Bangalore Collaborative Ph.D. Position

Date: 27/11/2026

Applications are invited from interested and motivated candidates for a fully funded PhD position under the BITS Pilani-IISc Bangalore collaborative Ph.D. program to work in the following project entitled "**Impact of data centres on grid protection : analysis and mitigations**" under the supervision of **Prof. Alivelu Manga Parimi (Supervisor from BITS Pilani, Hyderabad Campus)** [URL: https://www.bits-pilani.ac.in/hyderabad/alivelu-manga-parimi/] and Prof. Sarasij Das **(Co-supervisor from IIsc Bangalore)** [URL: https://eecs.iisc.ac.in/people/sarasij-das/]

This project will develop protection-oriented models of data-centre loads, including UPS ride-through/transfer behaviour and converter-limited DC-side faults. It will analyse the impact on overcurrent, directional and distance protection, and on coordination between internal data-centre protection and upstream utility relays, within a single framework. Based on these findings, the project will develop an adaptive, optimized/ML -assisted relay-setting strategy and benchmarked against conventional fixed settings.

Number of Vacancy	01
Essential Qualification	M.Tech. / M.E. / B.E. / B.Tech. in EEE/Energy Engineering or other allied disciplines from a recognized university, with minimum 70%/7CGPA aggregate marks or equivalent CGPA.
Desired Skills	Power system modelling and simulation in MATLAB/Simulink, or equivalent tools. Understanding of power electronic converters (rectifiers, inverters, UPS topologies) and their fault behaviour. Programming proficiency in Python and/or MATLAB for data analysis
Fellowship	₹ 37,000 + 30% HRA = ₹ 48100 per month Annual Contingency: ₹ 20,000/-
Age Limit	28 Yrs.

How to apply: Interested candidates meeting above-mentioned qualification and relevant experience can submit their CV giving detailed information about the educational qualifications (marks list and certificates), research experience and published research papers if any, by 6th Dec. 2026 at this google link https://forms.gle/ScFZsv1uCdEUx26F6 . Please note that only shortlisted candidates will be called for online interviews. The interview schedule will be sent to candidates via email. Selected candidates will be registered for the Ph.D. program subject to the fulfilment of the requirements of the Ph.D. qualification process as per academic guidelines of BITS Pilani. https://admissions.bits-pilani.ac.in/PhD/phd.html

Notes:

1. The student will be registered at BITS Pilani and, upon successful completion of all programme requirements, the PhD degree will be awarded by BITS Pilani.
2. The duration of the Ph.D. student's stay at **IISc Bangalore** will be mutually decided by the supervisor and Co-Supervisor based on the requirements of the research work. However, student is expected to spend at least 6 months, which could be extended up to 24 months at **IISc Bangalore** during the course of the PhD program.
3. For any queries regarding the position, please feel free to email at **alivelu@hyderabad.bits-pilani.ac.in**
4. No TA/DA will be paid for attending the interview.

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