



Birla Institute of Technology & Science, Pilani
K K Birla Goa Campus

JRF position under DRDO funded project

Applications are invited for the position of Junior Research Fellow in the Department of Electrical and Electronics Engineering at BITS Pilani, K. K. Birla Goa Campus, Goa for a DRDO sponsored research project under CARS Scheme.

Principal Investigator: Dr. Ashish Chittora

Broad area of Research: High Power RF/Microwave Electronics and Waveguide Component Design

Duration of the project: One year

Fellowship Amount (Rs.): 37000 per month

Accommodation: Hostel accommodation will be provided subject to availability and will be governed by the rules and regulations of the Institute prevailing at the time of joining of the candidate.

Qualification: B.E./ B. Tech (Electronics/ Electronics & Communication/ Electrical and Electronics Engineering) in first division with NET/GATE qualification.

OR

M. Tech / M.E. (Electronics / Electronics & Communication/ Applied Electronics/ RF and Microwave/ Microwave Engg.) in first division at both Graduate and Post graduate level.

How to Apply: Interested candidates may send application and curriculum vitae with details of research experience to the principal investigator (email: ashishc@goa.bits-pilani.ac.in) by **26 October, 2026**. Only shortlisted candidates will be contacted via e-mail for online/Personal interview to be held at BITS Pilani, Goa Campus and no TA/DA will be paid for the Interview.

Selected candidate will get a chance to apply for PhD degree at BITS Pilani, K. K. Birla Goa Campus (Rules of PhD program for BITS Pilani are applicable)