Birla Institute of Technology & Science, Pilani
Hyderabad Campus

Dated: 22/09/2025

Applications are invited for the post of Junior Research Fellow (JRF)

Project Title : **Power and Performance Estimation based on RTL Code**

Funding Agency : Google

Project Location : BITS Pilani, Hyderabad Campus

Duration : 1 Year (12 Months)

Position: Junior Research Fellow (JRF)

Fellowship: Rs. 37,000

Essential Qualification: B.E/B.Tech in ECE/CSE

Desired Qualification: M.E/M.Tech in Microelectronics and VLSI/Embedded Systems/CSE or related specializations.

Desired Expertise: Strong understanding of RTL Design
Knowledge of Computer Architecture
Proficiency in Verilog
Experience with Vivado Tool/Cadence Tools/Synopsys Tools
Candidates having strong expertise in working with Machine
Learning and Large Language Models may also be considered

Interested candidates should apply by filling out the form https://forms.gle/JiHkgJDNZp7Rd51E8 and submit on or before **13.10.2025 (Monday)**

**Selected candidates shall get the additional opportunity to be enrolled for the Full-Time Ph.D program of BITS Pilani, Hyderabad Campus, based on the institutional rules and regulations.

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