CALL FOR JUNIOR RESEARCH FELLOW FOR AN INDUSTRY PROJECT

Date: 05/08/2025

Applications are invited from suitable candidates for **Junior Research Fellow (JRF)** to work on the following project sponsored by **AMD**, under the supervision of **Dr. Meera Kumari**, Department of **Electrical and Electronics Engineering**, BITS-Pilani, Hyderabad campus.

Title of the project: Research on Bandgaps, Oscillators, Glitch-Detectors, ADCs, Low-Power SRAM Architectures, etc.

Duration of the Project: 2 years

Project position and number: One (01)

Fellowship amount: ₹45,000/month

Eligibility requirements: ME/MTech in Microelectronics, VLSI Design or BTech/MTech/BE in Electrical and Electronics, with strong fundamentals in Analog VLSI Circuit Design

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Interested candidates with the above-mentioned qualification can send the applications on a plain paper giving detailed information about the educational qualifications, research experience and published research papers if any, by **12/08/2025**, to the Principal Investigator**, Dr Meera Kumari** via email: **meera.kumari@hyderabad.bits-pilani.ac.in** Please note that only qualified and suitable candidates will be called for interview to be held at BITS - Pilani, Hyderabad Campus in the week of **18–22 August 2025** and no TA/DA will be paid for the interview. Selected candidate may be permitted to register for Ph.D programme of BITS, Pilani, Hyderabad campus, subject to the fulfillment of the requirements.

Dr. Meera Kumari
Assistant Professor
(Principal Investigator)

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