Birla Institute of Technology & Science, Pilani
K K Birla Goa Campus

Research Associate (JRF) position under a CREST-funded project

Applications are invited for the position of **Junior Research Fellow (JRF)** in the Department of Electrical and Electronics Engineering at BITS Pilani, K. K. Birla Goa Campus, Goa, for a Centre for Research Excellence in Semiconductor Technologies (CREST) sponsored research project titled "Development of RISC-V SoC Infrastructure with Multi-Level Co-Simulation, RTL Correlation, and Full-Lifecycle Verification Environments", sanction number: ARCH/09/26/101.

Principal Investigator: Prof. A Amalin Prince

Co-Principal Investigator: Dr. Srikanth Neelam, Founder & CEO, VConnecTech Systems, Hyderabad

Broad area of Research: VLSI Architecture

Duration of the project: 1 year

Fellowship Amount (Rs.): 37000/- per month

Accommodation: Hostel accommodation will be provided, subject to availability, and will be governed by the Institute's rules and regulations in force at the time of the candidate's joining.

Qualification: Degree in Engineering with relevant hands-on experience.

How to Apply: Interested candidates may send their application and curriculum vitae, including details of experience, to the principal investigator (email: amalinprince@goa.bits-pilani.ac.in) by 30th of November 2026. Only shortlisted candidates will be contacted via e-mail for an online/Personal interview to be held at BITS Pilani, Goa Campus and no TA/DA will be paid for the Interview.

Selected candidate will get a chance to apply for a PhD degree at BITS Pilani, K. K. Birla Goa Campus (Rules of the PhD program for BITS Pilani are applicable)

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