



Birla Institute of Technology & Science, Pilani

Pilani Campus

Grants Consultancy and Industrial Research (GCIR)

Call for Research Staff Position (Senior Project Engineer/Scientist)

Date: 08 April 2026

About: Applications are invited from interested and motivated candidates for the post of Senior Project Engineer/Scientist on the research project sponsored by the Ministry of Electronics and Information Technology (MeitY) . The position will be purely on contractual basis as per the following details:		
Position	Senior Project Engineer/Scientist	Number of Vacancy: 03
Funding Agency	Ministry of Electronics and Information Technology (MeitY)	
Project Title	Design and Development of an AI/ML-Driven Toolkit for Automated Cybersecurity Audits and Compliance Framework	
Principal Investigator	Dr. Vishal Gupta, Department of Computer Science and Information Systems, BITS Pilani, Pilani Campus	
Project Tenure	2 Years	
Project/Job Description	<p>The role involves contributing to a MeitY-sponsored R&D project jointly undertaken by BITS Pilani and IIT Delhi, focused on the design and development of an AI/ML-driven cybersecurity audit and compliance framework for government and critical infrastructure systems.</p> <p>As a Senior Project Engineer/Scientist, you will play a key role in bridging technical leadership and hands-on development. You will contribute to system design, guide implementation efforts, and support project execution while working closely with Principal investigators and mentoring junior team members.</p>	
Qualification	M.Tech./MS(R)/MCA first class or equivalent in the appropriate discipline related to Computer Science or related/equivalent OR B.Tech./M.Sc./Post Graduate in relevant Computer Science discipline (all first class or equivalent) with 3 years' experience.	
Upper Age Limit	NA	
Fellowship/Stipend	Rs. 60750/- to 70420/- per month	
Registration for PhD	Selected candidates may be permitted to register for the PhD program of BITS Pilani, subject to fulfilling the requirements of the PhD qualification process as per the institute norms.	
How to Apply	Email the following documents with subject line "Senior Project Engineer Position" to: vishalgupta@pilani.bits-pilani.ac.in a. CV (Mention past education, technical skills, research experience, publications, etc.) b. Cover letter and contact details of at least two academic referees. c. Copy of the NET/GATE/National-level exam score card (if applicable).	
Selection Process	Candidates will be shortlisted based on their merit, experience, and as per the requirements of the project. Shortlisted candidates will be informed of the interview, which will be conducted through online mode. Only qualified and suitable candidates will be called for an interview.	

Application Deadline: 20th April, 2026

