



Birla Institute of Technology & Science, Pilani

Pilani campus

Grant Consultancy and Industrial Research Division

Center for Research Excellent in Semiconductor Technologies (CREST)

CALL FOR JUNIOR RESEARCH FELLOW AT BITS PILANI

Date: 10-10-2023

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| About: Applications are invited from interested and motivated candidates for the post of Junior Research Fellow in a project under Center for Research Excellent in Semiconductor Technologies (CREST), BITS Pilani. The details are as follows. | |
| Position: | Research Fellow |
| Number of Vacancy: | 01 |
| Project Title: | Design & Development of Silicon Prototypes of ADC/DAC/SRAM Memory around RISC Processor |
| Investigators: | Dr. Nitin Chaturvedi & Prof. Anu Gupta, Department of EEE, Pilani Campus |
| Project Tenure: | 2 years with possibility of extension to enroll to the Institute Ph.D. program. |
| Job Description: | Research Fellow will carry out the experimental/simulation work including system design, circuit analysis, physical design and testing. |
| Essential Qualification and specializations: | M.E/M.Tech (Microelectronics/VLSI Design/Embedded Systems) with at least 60% marks. |
| Technical Skill: | Knowledge of VLSI Design Flow, RTL, Digital/Analog Circuit Design Expertise in EDA tools |
| Fellowship: | Rs. 37000 - Rs. 42000 per month (based on the qualification, performance and the year of PhD) |
| Registration for PhD: | Selected candidates will be given the opportunity for a full time PhD program at BITS, Pilani as per the institute norms. For more information visit https://www.bitsadmission.com/phdmain.aspx |

How to Apply:

Interested candidates should apply to this advertisement, by sending their biodata to nitin80@pilani.bitspilani.ac.in. Shortlisted candidates will be informed for the interview. Mere possession of minimum qualification does not guarantee an invitation to the interview. Candidates will be shortlisted based on their merit and as per the requirement of the project.

Application Deadline: 21-10-2023

