



Birla Institute of Technology & Science, Pilani

Pilani Campus

Sponsored Research and Consultancy Division (SRCD)

CALL FOR JUNIOR/SENIOR RESEARCH FELLOW AT BITS PILANI

Date: 29-11-2025

About: Applications are invited from interested and motivated candidates for the post of Junior Research Fellow in a project funded by Indian Space Research Organisation (ISRO) . The details are as follows.	
Position:	Junior Research Fellow / Senior Research Fellow (JRF/SRF)
Number of Vacancy:	01
Project Title:	Design & Characterization of Logic-in-Memory Computation within SRAM array
Investigators:	Dr. Nitin Chaturvedi, Department of EEE, Pilani Campus
Project Tenure:	2 years with possibility of extension to enroll to the Institute Ph.D. program.
Job Description:	Research Fellow will carry out the experimental/simulation work including system design, circuit analysis, physical design and testing.
Essential Qualification and specializations:	M.E/M.Tech (Microelectronics/VLSI Design/Embedded Systems/Electronics) or B.Tech/B.E/M.Sc (Electronics) with good academic record
Technical Skill:	Knowledge of VLSI Design, Digital/Analog Circuit Design, RTL, Verilog Expertise in EDA tools
Fellowship:	Rs. 37000 - Rs. 42000 per month (based on the qualification, performance and the year of PhD)
Registration for PhD:	Selected candidates will be given the opportunity for a full time PhD program at BITS, Pilani as per the institute norms. For more information visit https://www.bitsadmission.com/phdmain.aspx

How to Apply:

Interested candidates may apply immediately to this advertisement, by sending their CV to nitin80@pilani.bits-pilani.ac.in or contact at **8290287578**.

