



Birla Institute of Technology & Science, Pilani

Hyderabad Campus

CALL FOR JUNIOR/SENIOR RESEARCH FELLOW AT BITS PILANI, HYDERABAD CAMPUS

Date: 15-09-2026

About: Applications are invited from interested and motivated candidates for the post of Junior Research Fellow in a project funded by Center for Research Excellence in Semiconductor Technologies (CREST) . The details are as follows.	
Position:	Junior Research Fellow
Number of Vacancy:	01
Project Title:	TinyEdge: An In-House Reusable Edge Computing SoC Platform for Low-Power Intelligent Systems
Investigators:	Dr. Kanika Monga, Department of EEE, Hyderabad Campus Dr. Nitin Chaturvedi, Department of EEE, Pilani Campus
Project Tenure:	Initially for 1 year, extendable up to 2 years, with the possibility of further extension for enrollment in the Institute's Ph.D. program.
Location	Hyderabad (On-Campus)
Job Description:	Research Fellow will work on the design, development, integration, and validation of System-on-Chip (SoC). The work will involve hardware architecture development, RTL design, processor/peripheral integration, and FPGA/ASIC-based prototyping.
Essential Qualification and specializations:	M.E/M.Tech (Microelectronics/VLSI Design/Embedded Systems/Electronics) or B.Tech/B.E/M.Sc (Electronics) with good academic record
Technical Skill:	Verilog/SystemVerilog/VHDL, FPGA/SoC design, computer architecture, embedded systems, RTL simulation and synthesis, C/C++/Python, AI/ML, low-power VLSI.
Fellowship:	Rs. 37000 per month
Registration for PhD:	Selected candidates may be permitted to register for the Ph.D. programme of BITS Pilani, subject to the fulfillment of the prescribed requirements

How to Apply:

Interested candidates may apply through the Google Form using the link provided: <https://forms.gle/p81BLym2s2vco4i56>



Scan QR code
to Apply

For any queries regarding the position or application process, please contact kanika.monga@hyderabad.bits-pilani.ac.in or nitin80@pilani.bits-pilani.ac.in

