



# Birla Institute of Technology & Science, Pilani

Pilani Campus

Sponsored Research and Consultancy Division (SRCD)

## CALL FOR PROJECT ASSOCIATE AT BITS PILANI

Date: 12-04-2026

<b>About:</b> Applications are invited from interested and motivated candidates for the post of <b>Project Associate</b> in a project funded by <b>Indian Space Research Organisation (ISRO)</b> . The details are as follows.	
<b>Position:</b>	<b>Project Associate</b>
<b>Number of Vacancy:</b>	<b>01</b>
<b>Location:</b>	<b>Hyderabad Campus</b>
<b>Project Title:</b>	<b>Design &amp; Characterization of Logic-in-Memory Computation within SRAM array</b>
<b>Investigators:</b>	<b>Dr. Kanika, Department of EEE, Hyderabad Campus</b> Dr. Nitin Chaturvedi, Department of EEE, Pilani Campus
<b>Project Tenure:</b>	<b>3 years with possibility of extension to enroll to the Institute Ph.D. program.</b>
<b>Job Description:</b>	<b>Project Fellow</b> will carry out the experimental/simulation work including system design, circuit analysis, physical design and testing.
<b>Essential Qualification and specializations:</b>	M.E/M.Tech (Microelectronics/VLSI Design/Embedded Systems/Electronics) or B.Tech/B.E/M.Sc (Electronics) with good academic record
<b>Technical Skill:</b>	Knowledge of VLSI Design, Digital/Analog Circuit Design, RTL, Verilog, Expertise in EDA tools
<b>Fellowship:</b>	<b>Rs. 30000 - Rs. 37000 per month</b> (based on the qualification, performance and the year of PhD)
<b>Registration for PhD:</b>	Selected candidates may be permitted to register for the Ph.D. programme of BITS Pilani, subject to the fulfilment of the prescribed requirements. For more information visit <a href="https://www.bitsadmission.com/phdmain.aspx">https://www.bitsadmission.com/phdmain.aspx</a>

### How to Apply:

Interested candidates may apply to this advertisement on or before 16/04/26, by sending their CV to [kanika.monga@hyderabad.bits-pilani.ac.in](mailto:kanika.monga@hyderabad.bits-pilani.ac.in).

