



# Birla Institute of Technology & Science, Pilani

## Hyderabad Campus

### **CALL FOR JUNIOR RESEARCH FELLOW FOR DST SERB PROJECT**

Date: 6<sup>th</sup> Aug, 2021

Applications are invited from suitable candidates for a **Junior Research Fellow (JRF)** position to work on a **SERB** sponsored project (**SRG/2020/00098**) under the supervision of **Dr. Parikshit Sahatiya**, Assistant Professor, Department of Electrical and Electronics Engineering, BITS-Pilani, Hyderabad campus.

Duration of the project: 18 Months

Project position: Junior Research Fellow

Number: 1

Fellowship amount: 31,000 pm consolidated

Eligibility requirements: M. Tech (VLSI, Microelectronics, Nanotechnology, ECE, EEE and allied areas). **GATE Qualification** is mandatory.

Interested candidates with the above mentioned qualification can send their applications by email giving detailed information about their educational qualifications, research experience and published research papers if any, by **17<sup>th</sup> August, 2021**, to the Principal Investigator at [parikshit@hyderabad.bits-pilani.ac.in](mailto:parikshit@hyderabad.bits-pilani.ac.in) with the subject line "**SERB SRG JRF Application**". Candidates should have a strong background in Device Physics and Solid State Electronics. Prior experience in **Device Fabrication, Synthesis and Characterization of Nanomaterials** is desirable.

Please note that only qualified and suitable candidates will be notified for Interview via email. Interview would be through Skype/Google Meet.

Selected candidate may be permitted to register for Ph.D. program of BITS, Pilani, subject to the fulfilment of the requirements.

Best Regards,  
Dr. Parikshit Sahatiya  
EEE Department, BITS Pilani Hyderabad Campus



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