



**Ref: SP/YO/2019/1104(G)**

**Date: 15<sup>th</sup> June 2020**

**JRF Recruitment in DST Sponsored Project (SP/YO/2019/1104(G))**

Applications are invited from eligible candidates to work as a Junior Research Fellow (JRF) in a Department of Science and Technology (DST) sponsored project at Electrical and Electronics Engineering Department, BITS-Pilani, (Pilani campus), Rajasthan. The details are as follows:

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| <b>Project Title</b>                     | Design and Development of Soft Robotic System for selective harvesting of Coffee cherries.   |
| <b>Project Duration</b>                  | 3 years  |
| <b>Principal Investigator (PI)</b>       | Dr. Meetha.V. Shenoy   |
| <b>Co-Principal Investigator (Co-PI)</b> | Dr. Nitin Chaturvedi   |
| <b>Project Description</b>               | Soft robotics system development is a highly interdisciplinary field. This project would focus on addressing the following issues in the context of selective harvesting:<br>1. To design & develop multi-sensor fusion based real-time control, guidance, and localization algorithms for the robotic arm system.<br>2. To design, develop, and implement machine learning-based techniques for target identification on FPGA/GPU.<br>3. To design & develop a novel soft robotic hand and integrate the same to the robotic arm system.<br>4. System Integration and Fine Tuning |
| <b>Fellowship Amount</b>                 | Rs 31,000/- pm (As per DST Norms)  |
| <b>Essential Qualifications:</b>         | M.E./M.Tech with at least 60% marks in any domain of Electrical and Electronics Engineering (e.g. Embedded systems / Microelectronics / Control Systems/ Mechatronics or related areas) or Computer Science.   |
| <b>Desirable Qualification:</b>          | Preference will be given to NET/GATE qualified candidates. Good written and spoken English, some experience of technical writing. Programming Skills in any of the following languages- VHDL/Verilog or Embedded C. Familiarity in using 3d printer.   |

**Application Procedure:**

- Candidates should email their CV to the PI (meetha.shenoy@pilani.bits-pilani.ac.in) by **17<sup>th</sup> July 2020** with the subject of the mail as 'Application for JRF position – DST project'.
- Shortlisted candidates will be informed through email and called for an interview. Interview will be conducted either online or in-person at the Electrical and Electronics Engineering Department, BITS-Pilani, (Pilani Campus), Rajasthan.

**Notes:**

1. For any queries regarding the position, please feel free to email the PI.
2. The position mentioned is temporary and for the period of duration of the project . If the performance of the candidate is found unsatisfactory, the position can be terminated with a 1-month notice.
3. The Selected candidate may be permitted to register for Ph.D.programme of BITS, Pilani, subject to the fulfillment of the requirements, as per institute rules.
4. No TA/DA will be paid for attending the interview.

**Dr. Meetha V Shenoy (PI)**

