

CALL FOR JUNIOR RESEARCH FELLOW FOR A SERB (SRG), DST, GOVT. of INDIA PROJECT

Date: 20/09/2020

Applications are invited from suitable candidates for **Junior Research Fellow (JRF)** to work on the following project sponsored by **SERB, DST**, under the supervision of **Dr. Karumbaiah (PI)**, in the Department of Electrical and Electronics Engineering, BITS-Pilani, Hyderabad campus.

Title of the project: Micro-plasma transistor for high temperature application.

Project position and number: 1 JRF

Fellowship amount: Rs. 31,000 pm [As per DST norms]

Duration: Initially for 1 year (extendable based on the performance)

Eligibility requirements: M.E./M.Tech. (**GATE is MUST**) in Electronics / Instrumentation / Nanotechnology / Materials Science/ Chemical Engineering/ Biomedical / Biotechnology or equivalent. Prior experience on experimental work in Electronic Device Fabrication, Chemical Sensing, Nano-materials, Plasma Science, Photo-catalysis, High Power Microwave Circuits, is highly desirable, but not mandatory. Candidates with publication(s) will be with priority. Candidates with UGC NET JRF / DST-Inspire will be given preference

Interested candidates can apply by sending **CV and Cover letter** (showing alignment and justification with the roles/responsibilities/requirements), by **30.09.2020 (last date)**, to the Principal Investigator Dr. Karumbaiah via email: karumcn@hyderabad.bits-pilani.ac.in. Please note that only qualified and suitable candidates will be called for interview to be held through google meet in the third week of Sept, 2020. Deserving JRF may be considered for Ph.D. program at BITS-Pilani if she/he meets the requirements of Ph.D. qualification process as per the institute norms.

Dr. Karumbaiah C N

Assistant Professor



Birla Institute of Technology & Science, Pilani
Hyderabad Campus, Jawahar Nagar, Shameerpet Mandal
Hyderabad 500078, Andhra Pradesh, India

Tel: +91 40 6630 3999
Fax: +91 40 6630 3998
Web: www.hyderabad.bits-pilani.ac.in