



# Birla Institute of Technology & Science, Pilani

Pilani Campus

Dr. NAVNEET GUPTA, Ph.D; FIE  
Associate Professor and Head  
Department of Electrical and Electronics Engineering

## Advertisement for the post of Junior Research Fellow (JRF)

Applications are invited from Indian nationals for the position of Junior Research Fellow (JRF) in a **DRDO-funded research project** (ERIP/ER/DG-MED & CoS/990416502/M/01/1657) with following details:

Title of the Project	Study on Electronic Transport Behaviour of Carbon Nanotube Based Field Effect Transistors (CNTFETs)
Sponsoring Agency	DRDO, New Delhi
Name of the Post	Junior Research Fellow (JRF)
Number of Post	01 (one)
Duration of project	3 years
Essential Qualification	ME/M.Tech in Electronic Devices, Nanoelectronics, Microelectronics or equivalent area with at least 60% marks.
Desirable Qualification	Practical Experience on device simulators such as SILVACO, Atomistic etc.
Fellowship	Rs. 25000/- p.m for two years and Rs. 28000/-p.m for Third Year
How to apply	Apply by sending an email with attached CV (must include list of publications and name of two references) on/before <b>30th September 2017</b> to "ngupta@pilani.bits-pilani.ac.in". After the preliminary review of the applications, the shortlisted candidates will be notified for personal interview through an e-mail. No TA/DA will be paid for attending the interview. The selected candidate can pursue the Ph.D at BITS Pilani after clearing the Institutional admission procedure. <b>(Those who have applied before for this advt. should apply again)</b>

Dr. Navneet Gupta,  
(Principal Investigator)  
Associate Professor and Head  
Department of Electrical and Electronic Engineering, BITS Pilani, Rajasthan



Birla Institute of Technology & Science, Pilani  
Pilani Campus, Vidya Vihar  
Pilani 333031, Rajasthan, India

Tel: +91 1596 515280  
email ngupta@pilani.bits-pilani.ac.in  
Web: www.bits-pilani.ac.in/~ngupta