



Birla Institute of Technology & Science, Pilani

Pilani Campus

Dr. NAVNEET GUPTA, Ph.D; FIE
Associate Professor and Head
Department of Electrical and Electronics Engineering

Advertisement for the post of Junior Research Fellow (JRF)

Applications are invited from Indian nationals for the position of Junior Research Fellow (JRF) in a **DRDO-funded research project** (ERIP/ER/DG-MED & CoS/990416502/M/01/1657) with following details:

Title of the Project	Study on Electronic Transport Behaviour of Carbon Nanotube Based Field Effect Transistors (CNTFETs)
Sponsoring Agency	DRDO, New Delhi
Name of the Post	Junior Research Fellow (JRF)
Number of Post	01 (one)
Duration of project	3 years
Essential Qualification	ME/M.Tech in Electronic Devices, Nanoelectronics, Microelectronics or equivalent area with at least 60% marks.
Desirable Qualification	Practical Experience on device simulators such as SILVACO, Atomistic etc.
Fellowship	Rs. 25000/- p.m for two years and Rs. 28000/-p.m for Third Year
How to apply	Apply by sending an email with attached covering letter, CV, list of publications and name of two references on/before 30th April 2017 to "ngupta@pilani.bits-pilani.ac.in". After the preliminary review of the applications, the shortlisted candidates will be notified for personal interview through an e-mail. No TA/DA will be paid for attending the interview. The selected candidate can pursue the Ph.D at BITS Pilani after clearing the Institutional admission procedure.

Dr. Navneet Gupta,
(Principal Investigator)
Associate Professor and Head
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